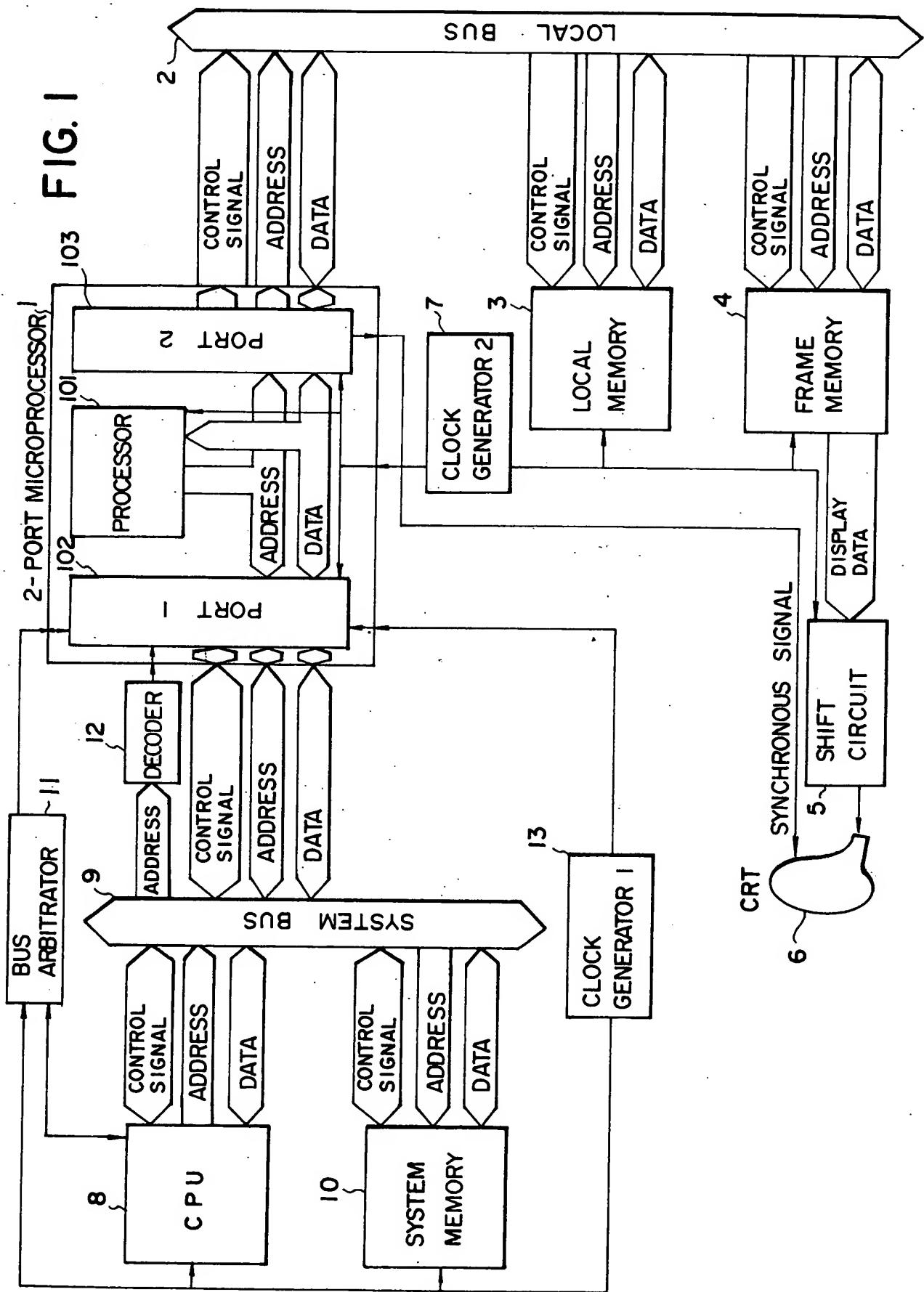
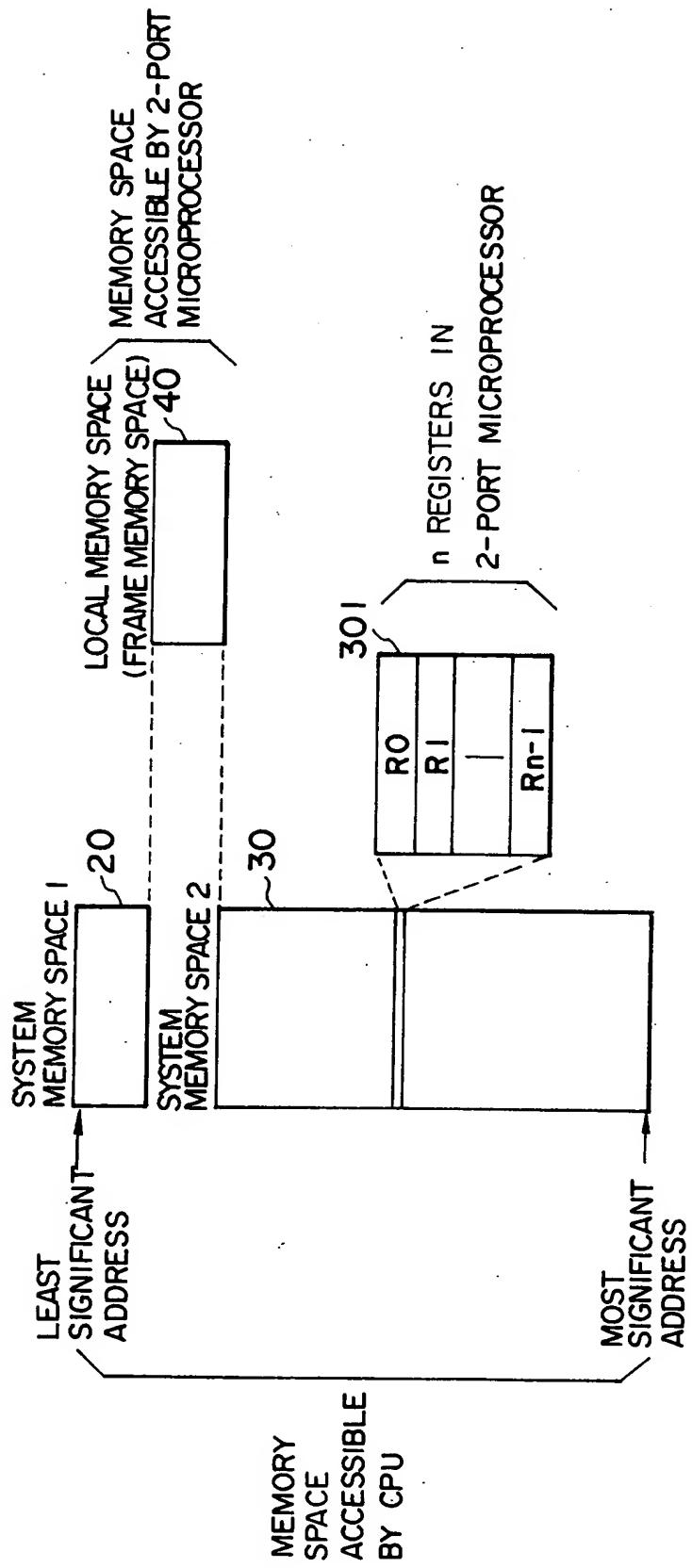


**FIG. I**

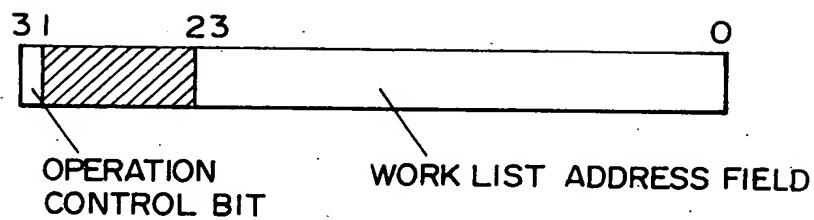


**FIG. 2**

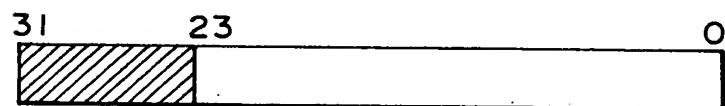


## FIG. 3

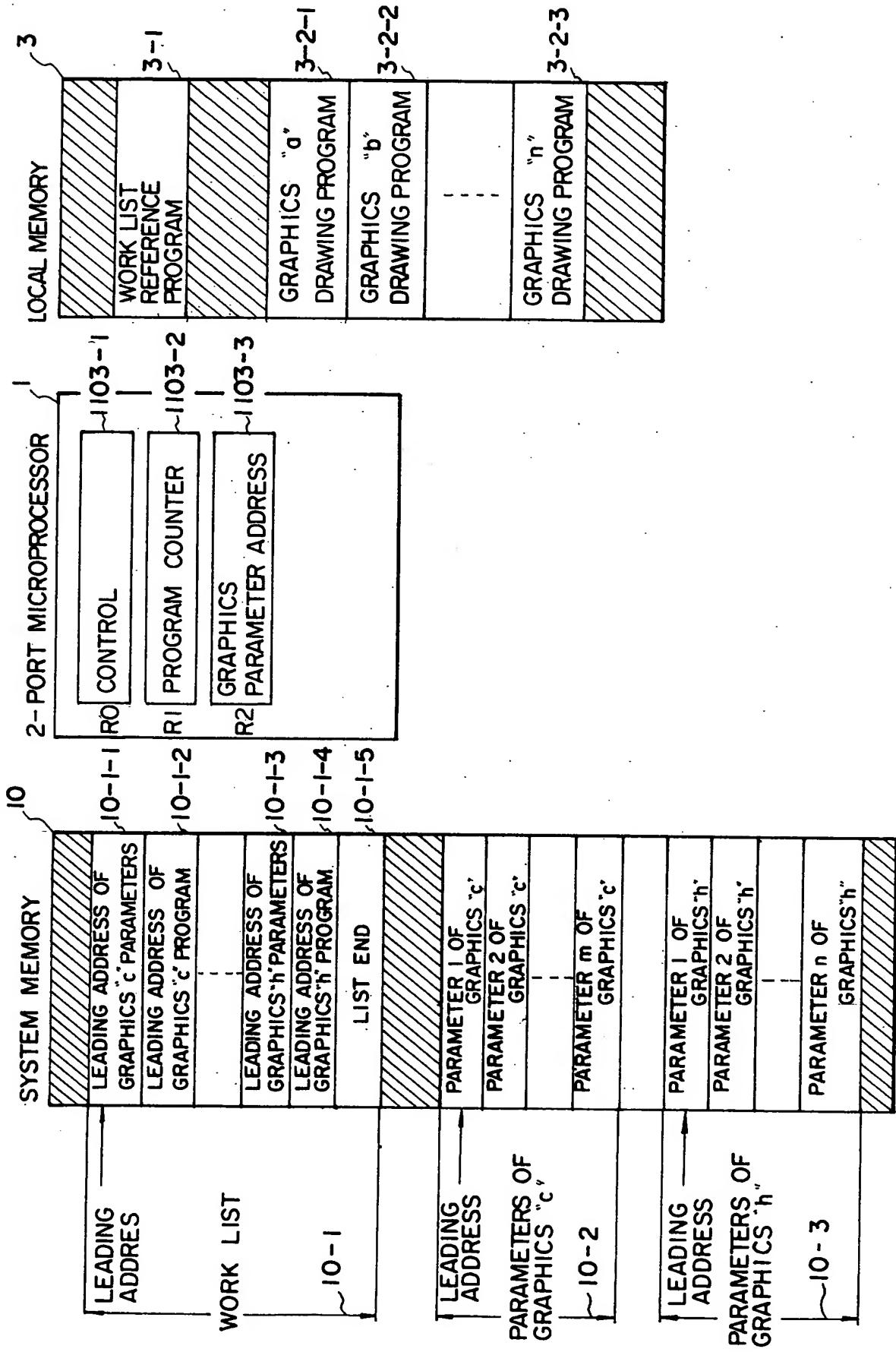
R0 (CONTROL REGISTER)



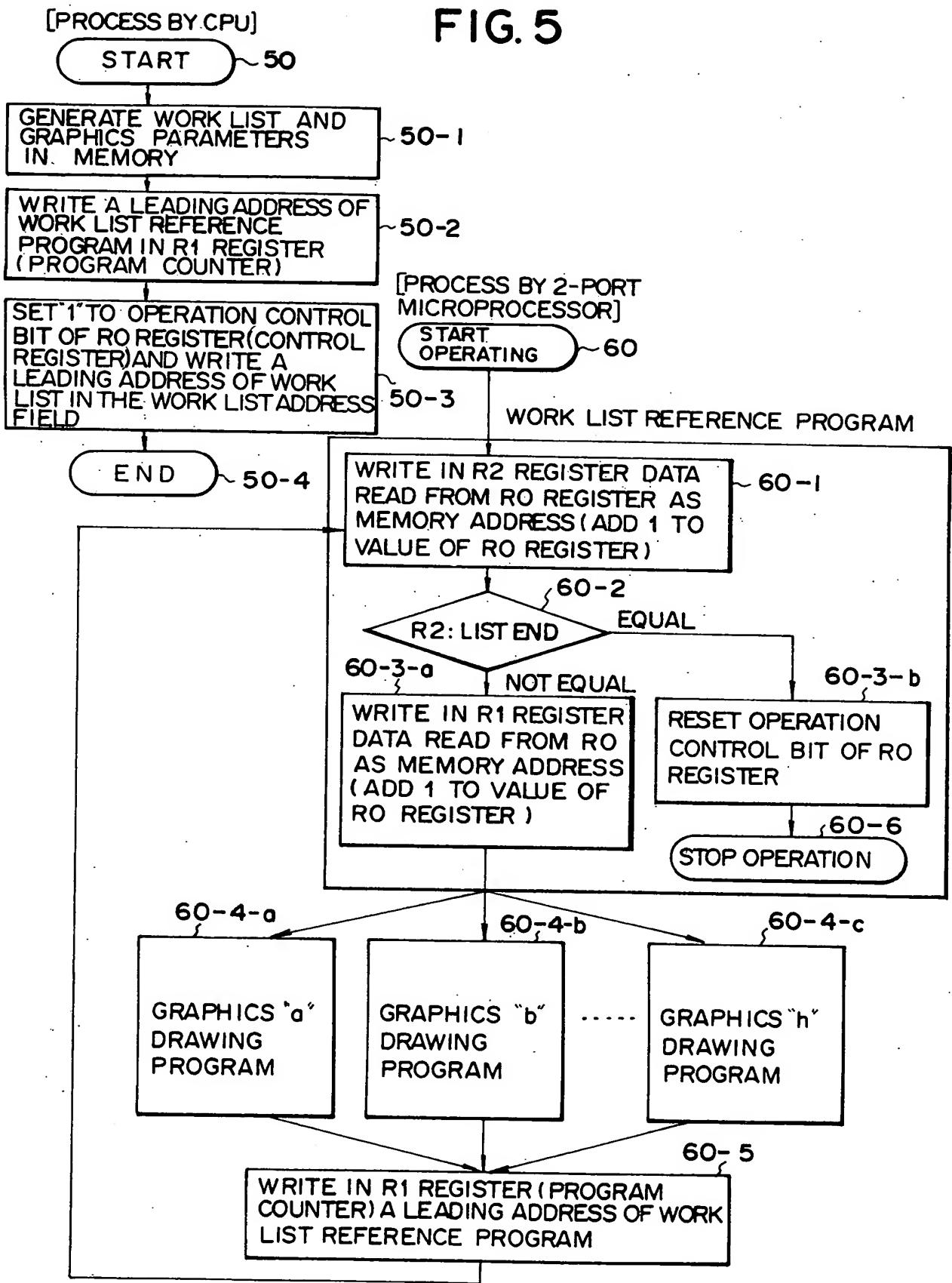
R1 (PROGRAM COUNTER)



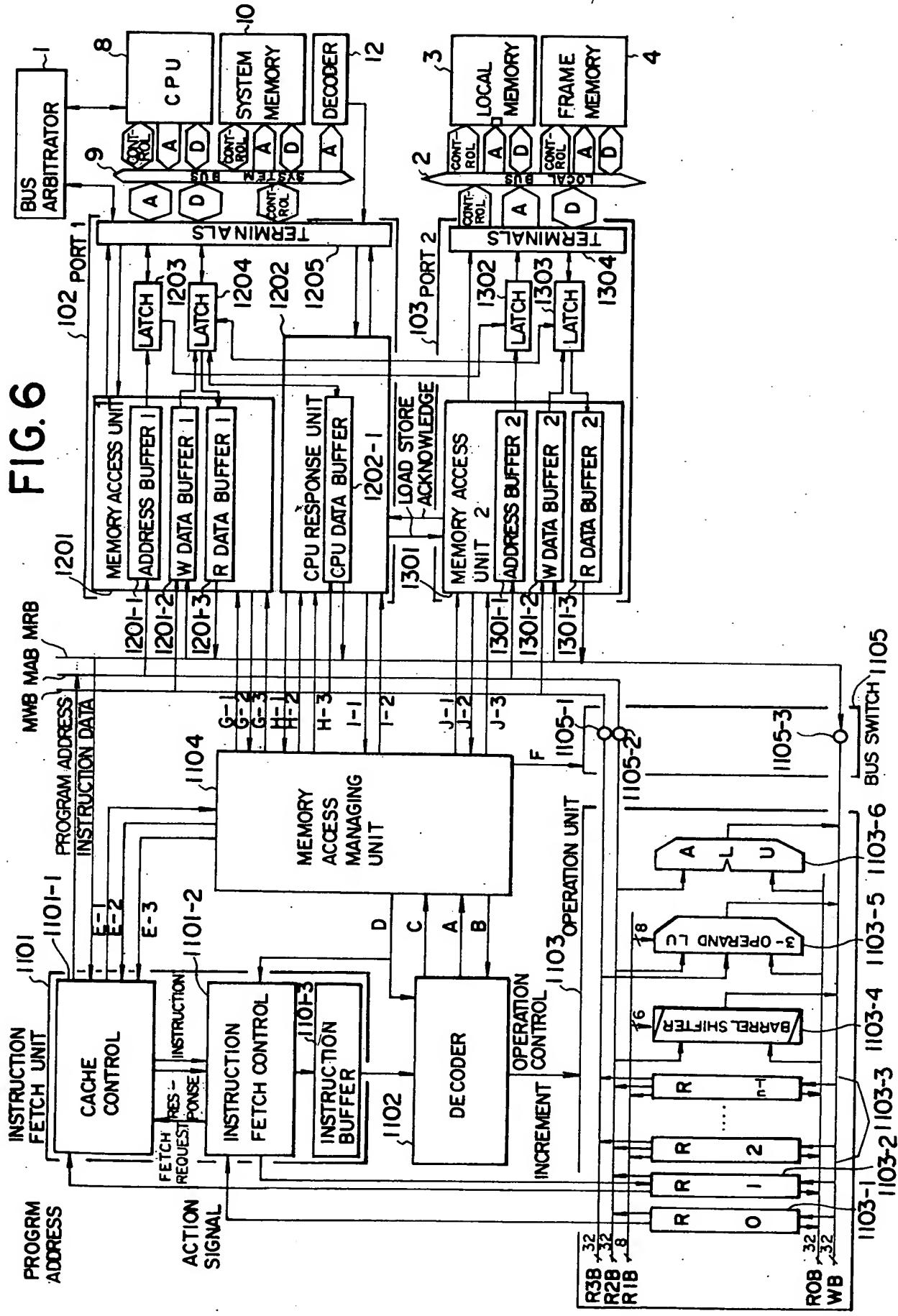
**FIG. 4**



**FIG. 5**

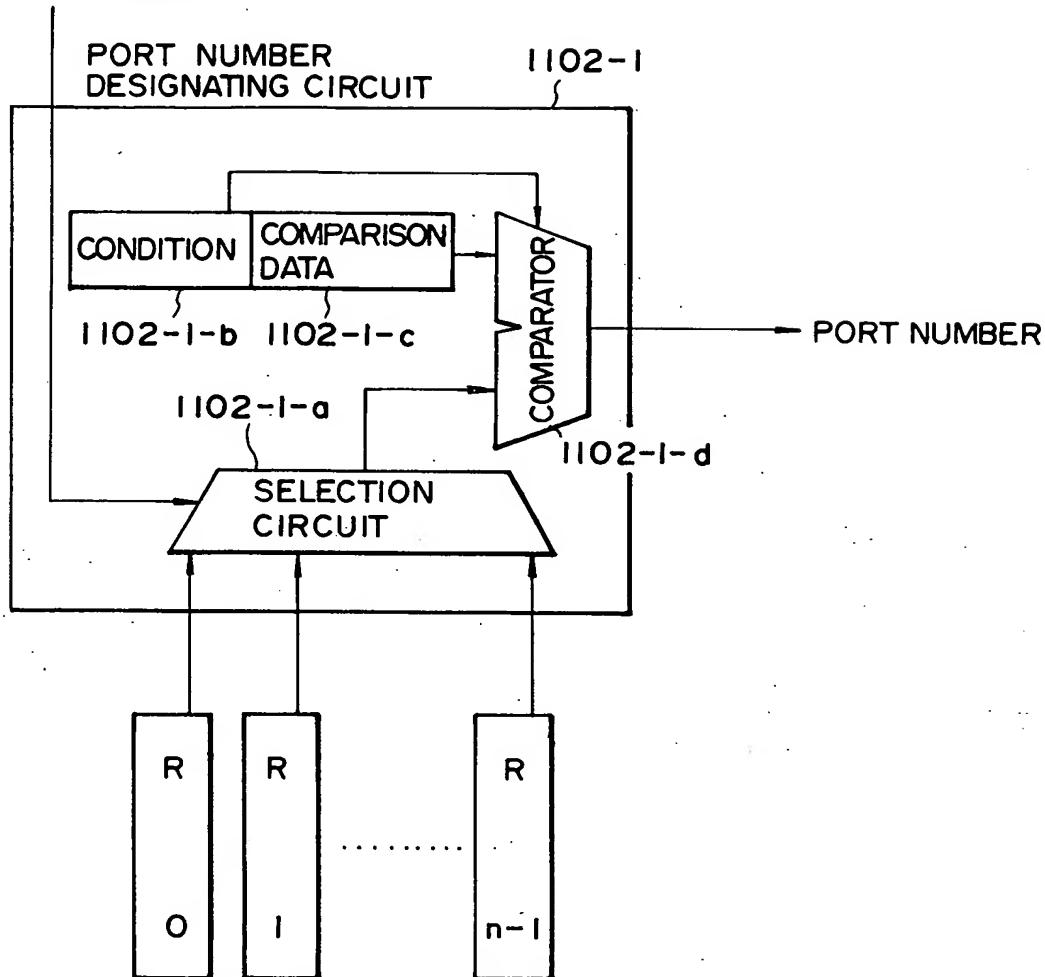


**FIG. 6**



# FIG. 7

ADDRESS  
REGISTER NUMBER



# FIG. 8

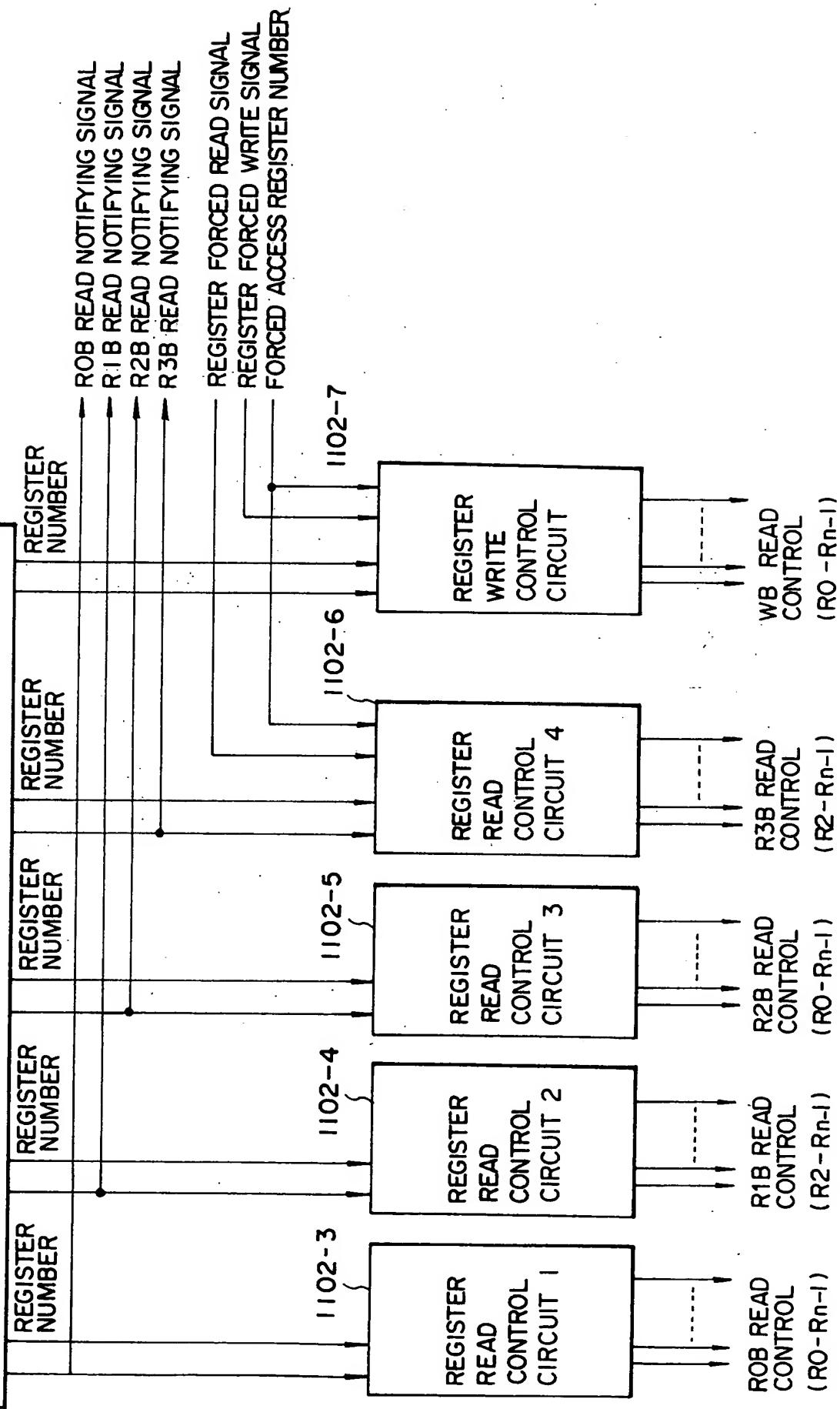
PORT DESIGNATING METHOD	CONDITION BIT
IF DATA OF ADDRESS REGISTER $\geq$ COMPARISON DATA, DESIGNATE PORT 1	0
IF DATA OF ADDRESS REGISTER $\geq$ COMPARISON DATA, DESIGNATE PORT 2	1

INSTRUCTION

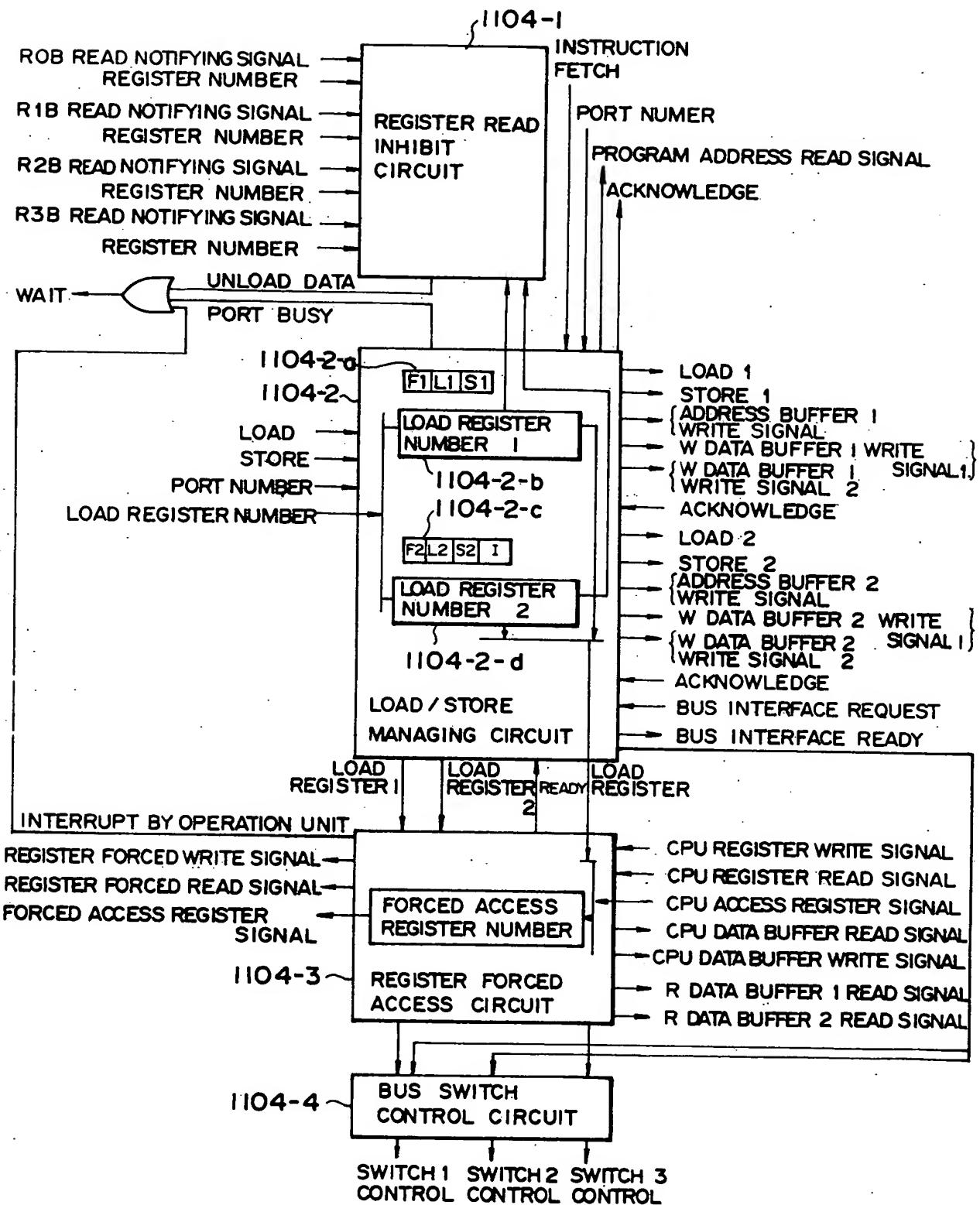
**FIG. 9**

1102-2

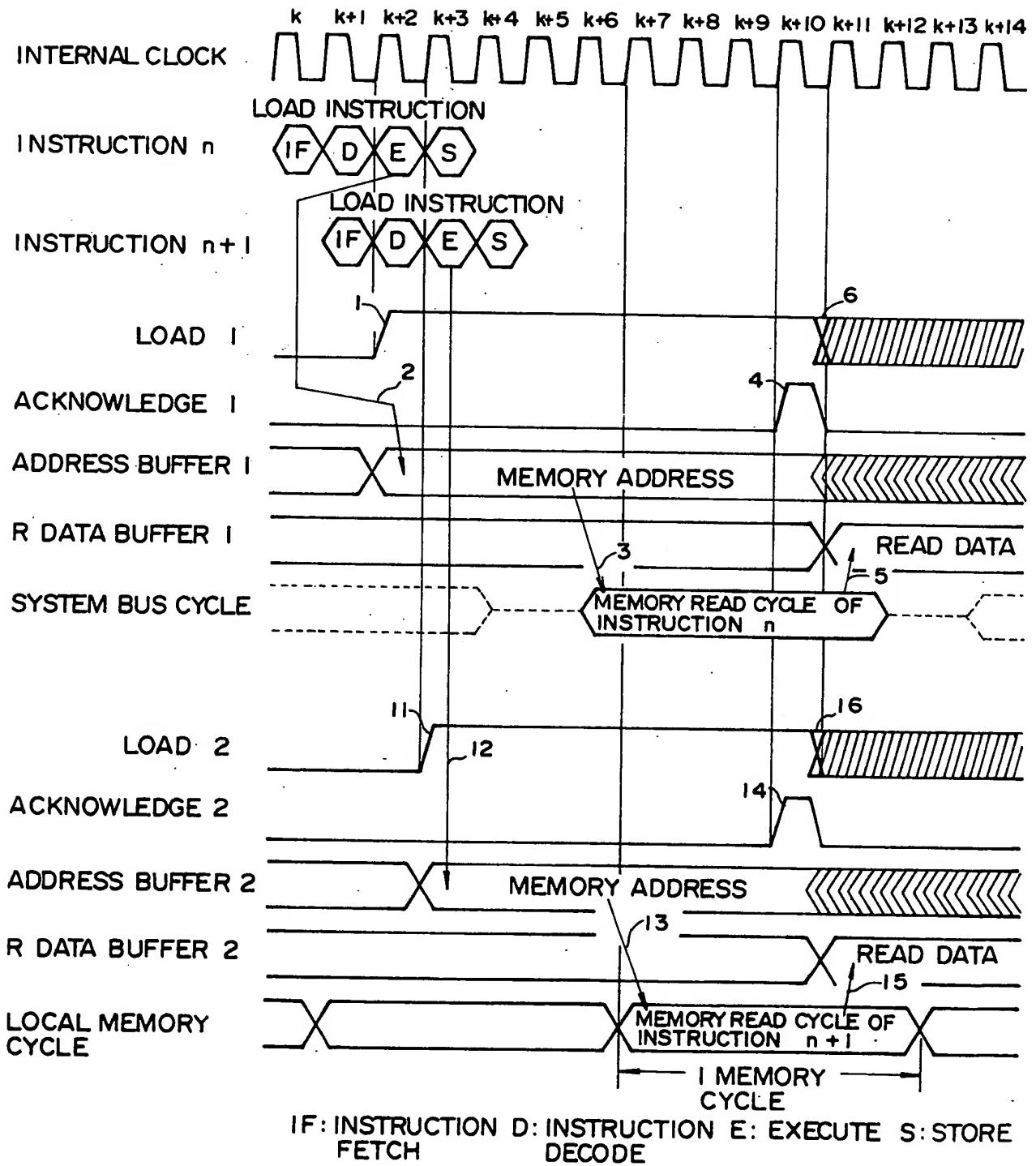
INSTRUCTION DECODER CIRCUIT



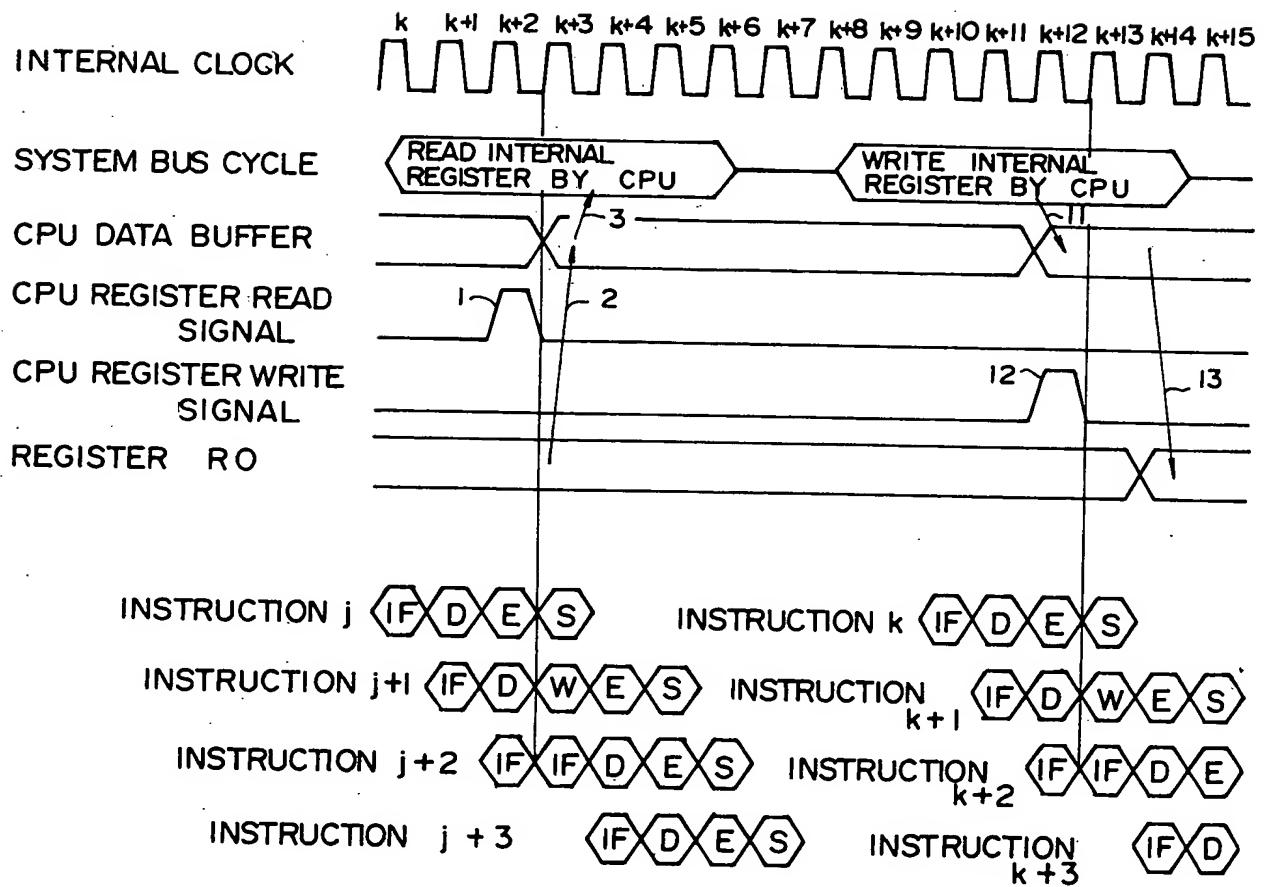
**FIG. 10**



**FIG. II**

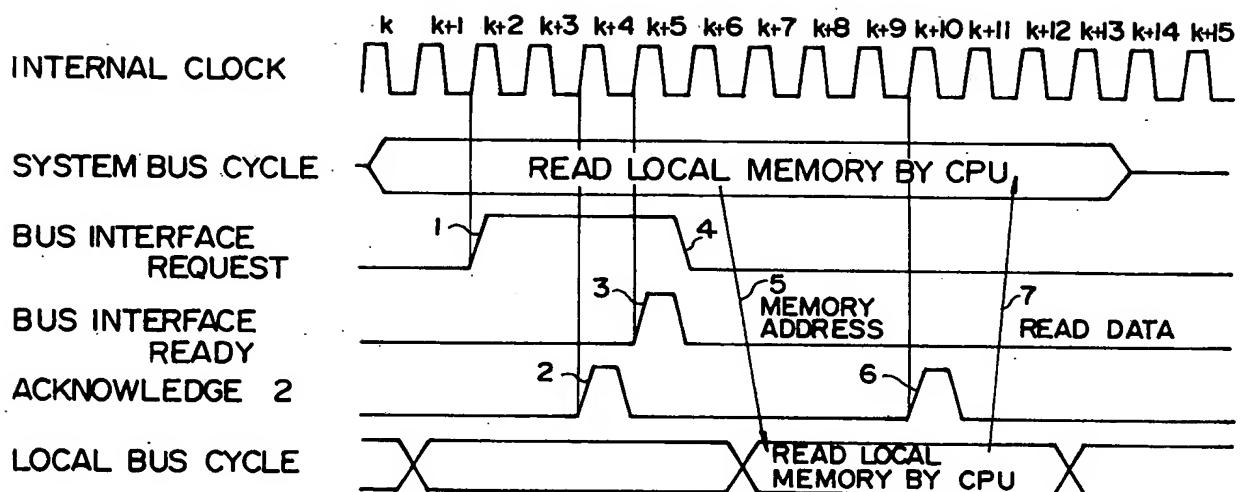


**FIG. 12**

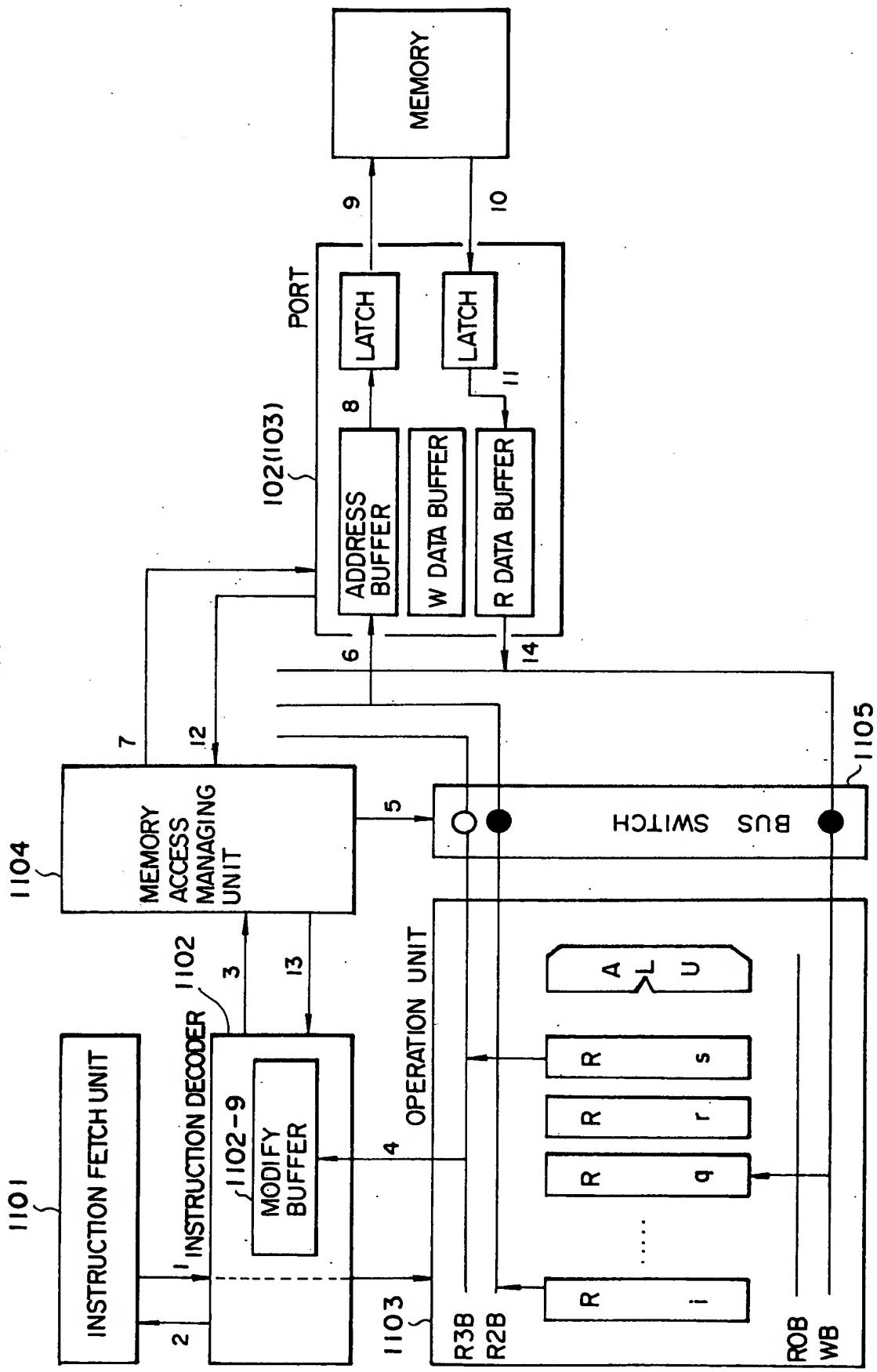


IF : INSTRUCTION   D: INSTRUCTION   E: EXECUTE   S: STORE   W: WAIT  
FETCH   DECODE

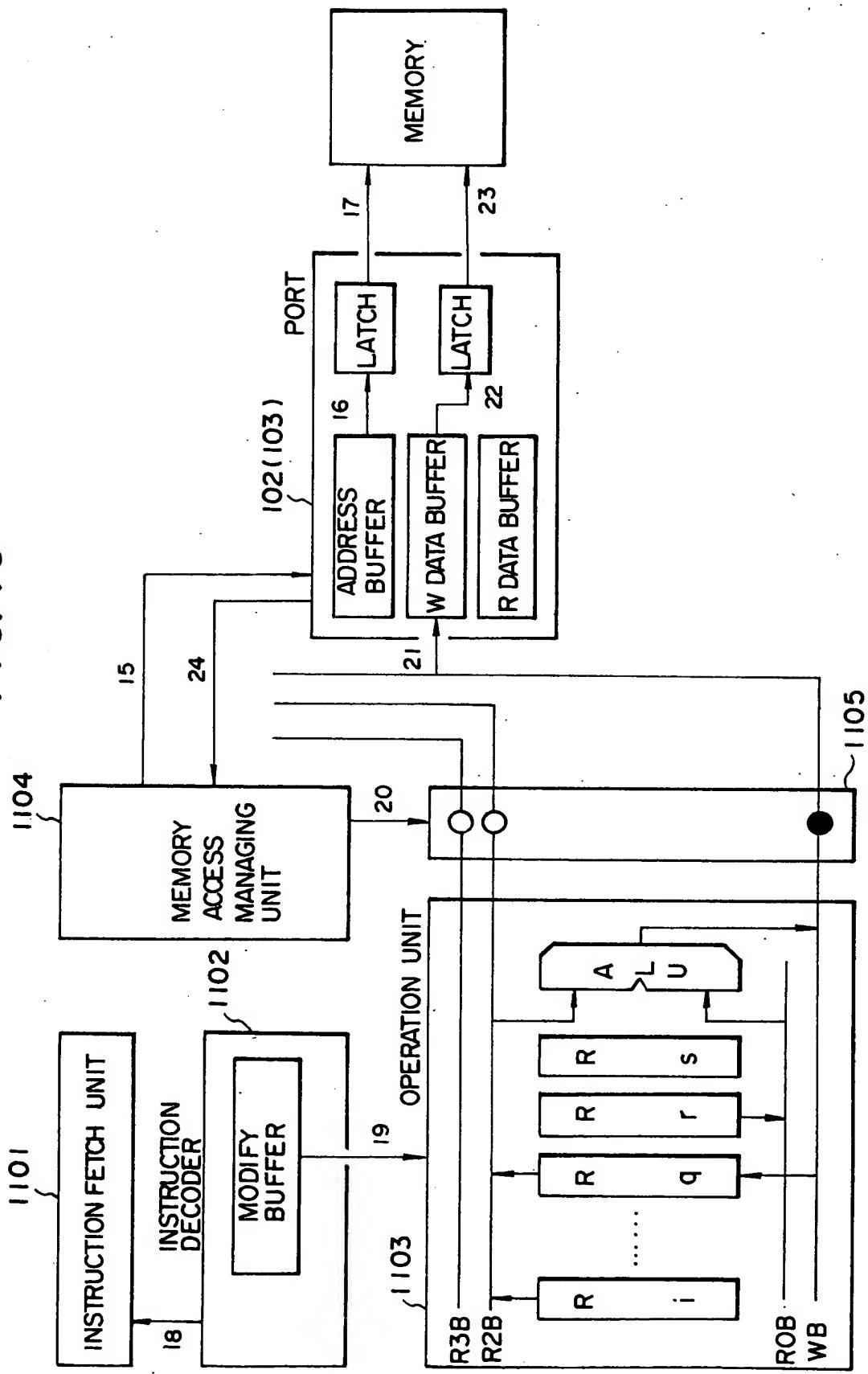
**FIG. 13**

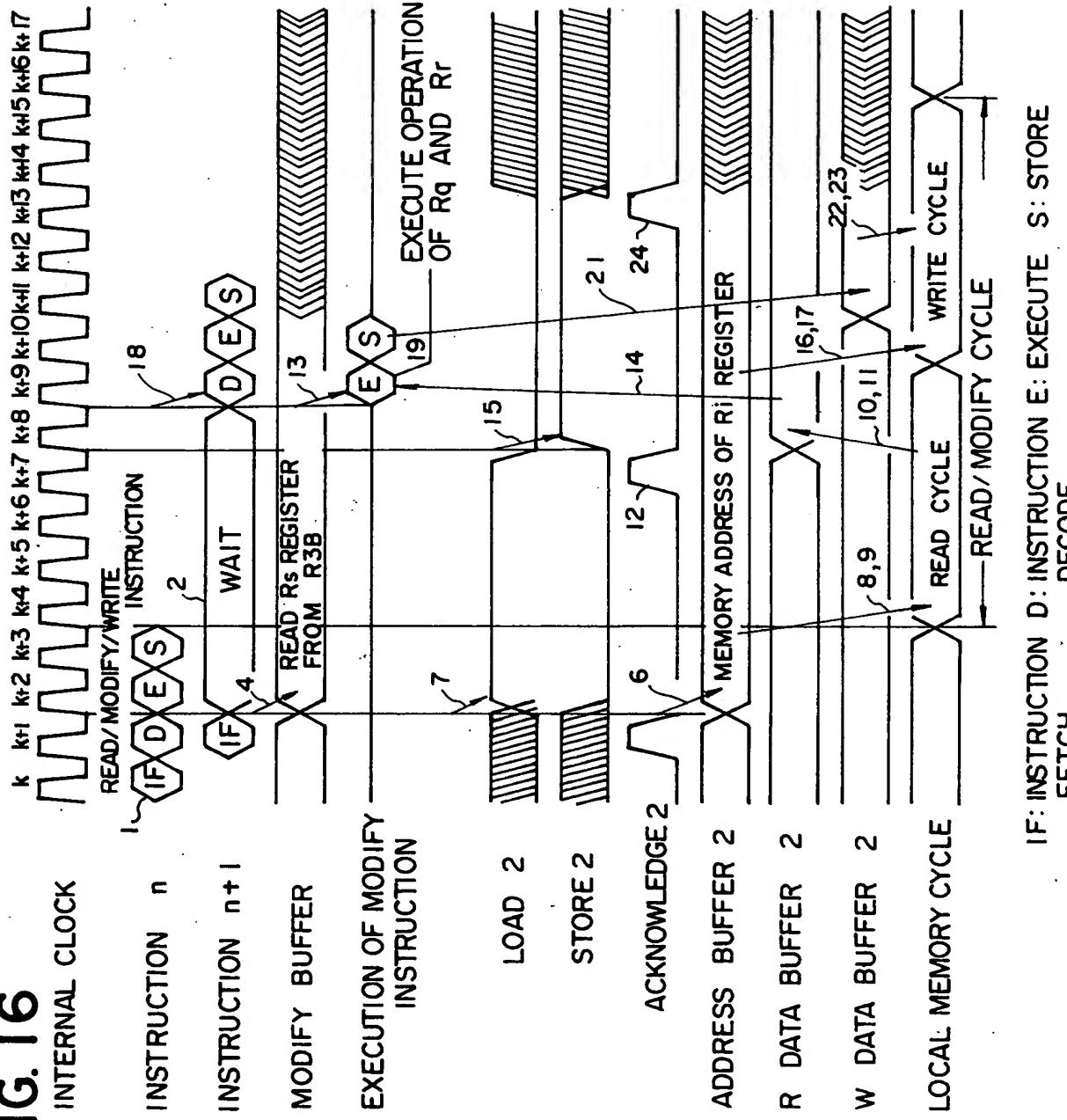


**FIG. 14**



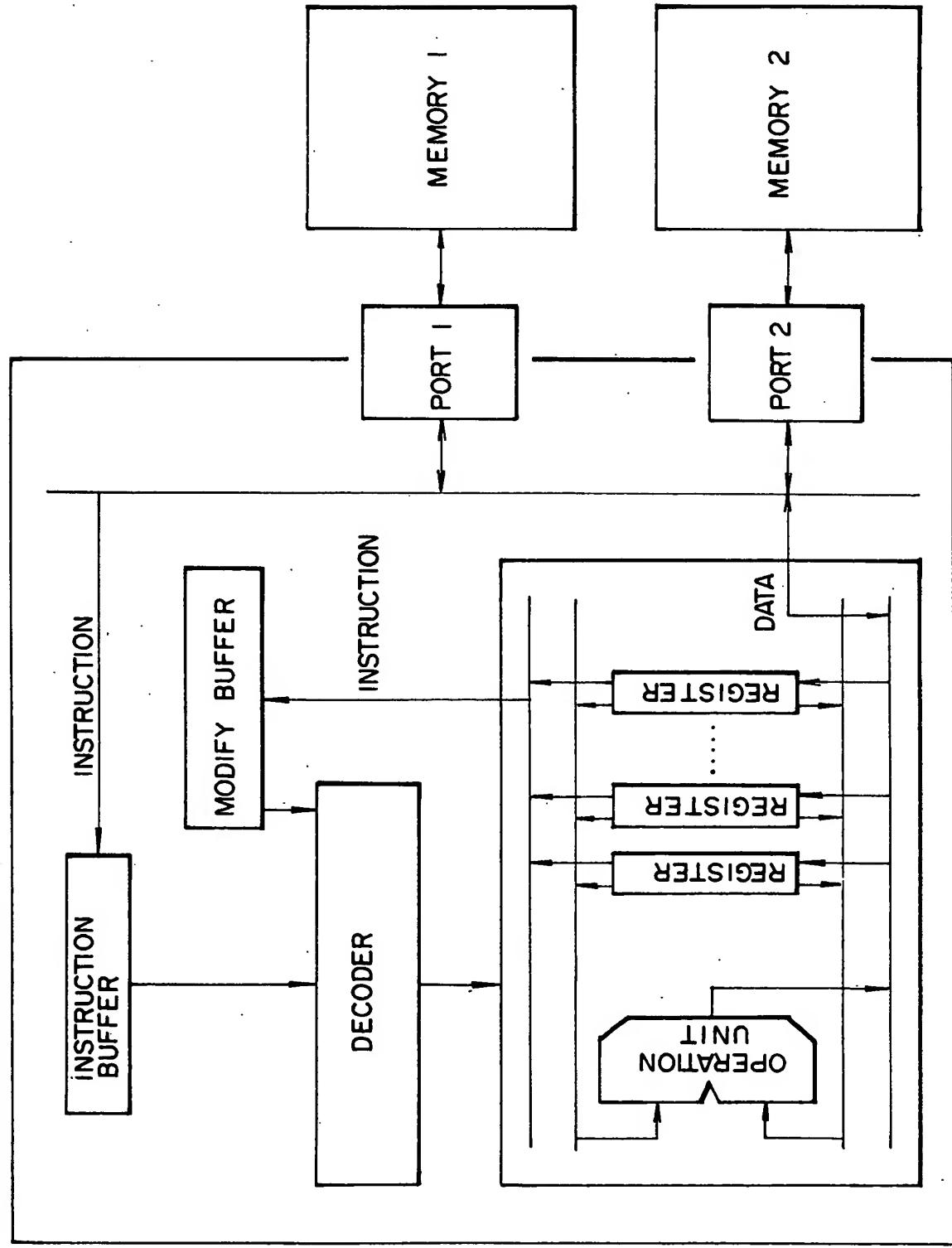
**FIG. 15**



**FIG. 16**

**FIG. 17**

2- PORT MICROPROCESSOR



**FIG. 18**

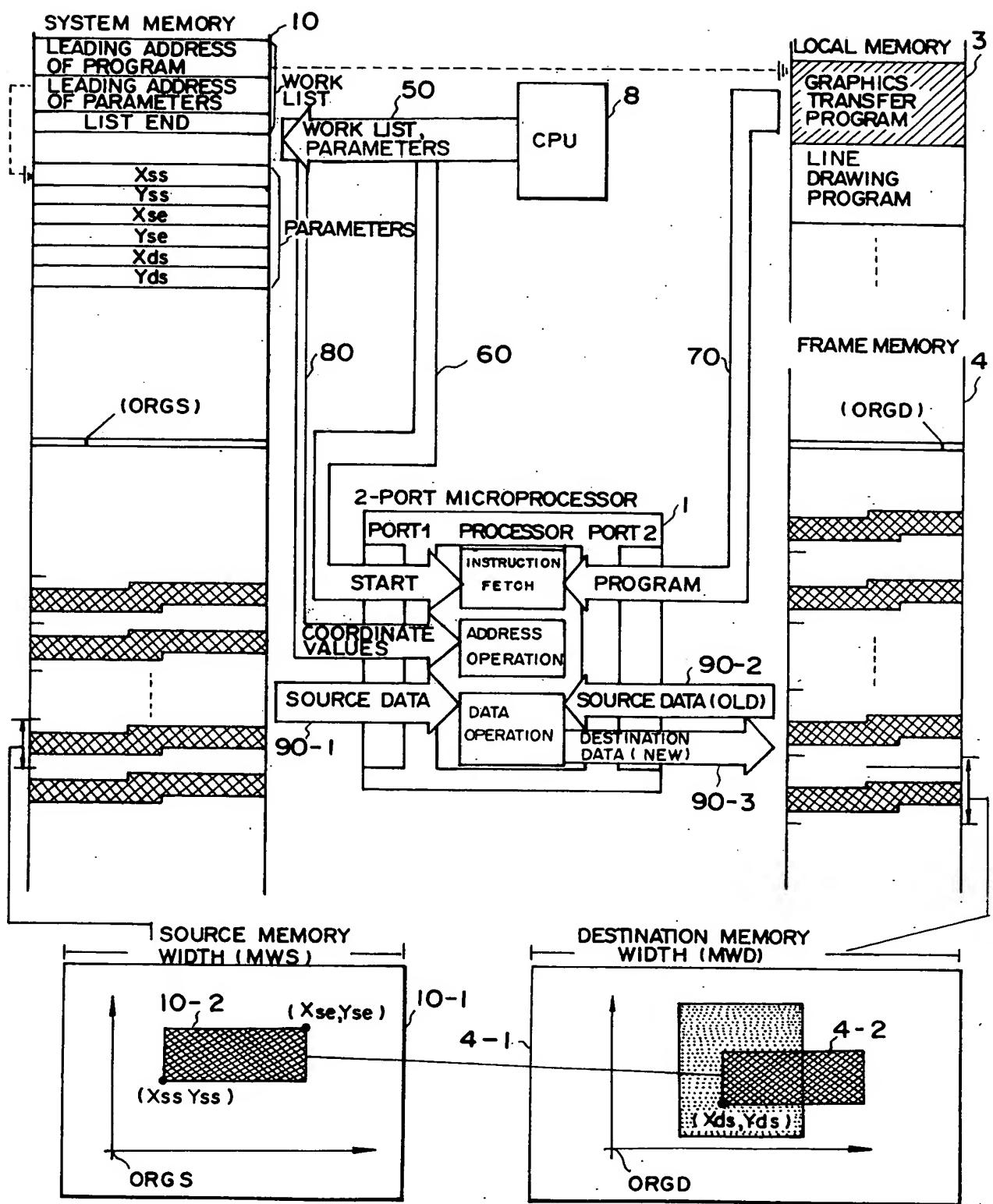


FIG. 19

